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APPLICANTS K. T. Hsiao, Taipei, TAIWAN; Hao-Liang Lo, Hsinchu, TAIWAN; Li-Yang Yang, Tainan Hsien, TAIWAN;					
** CONTINUING DATA *****					
** FOREIGN APPLICATIONS *****					
IF REQUIRED, FOREIGN FILING LICENSE GRANTED ** 08/17/2001					
Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no 35 USC 119 (a-d) conditions <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after met Allowance Verified and Acknowledged <i>[Signature]</i> <i>[Initials]</i>		STATE OR COUNTRY TAIWAN	SHEETS DRAWING 5	TOTAL CLAIMS 3	INDEPENDENT CLAIMS 2
ADDRESS J.C. Patents, Inc. Suite 114 1340 Reynolds Ave. Irvine, CA 92614					
TITLE Testing architecture for a semiconductor memory device					
FILING FEE RECEIVED 710	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:				
<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit					